



HSM150M12BM2

1.2kV, 12 mΩ All-Silicon Carbide Half-Bridge HIGHFEL Module

V_{DS}	1.2 kV
$I_{ID(@90^{\circ}C)}$	150A
$R_{DS(on)}$	12 mΩ

Features

- Low On-Resistance and High Current Density
- Low Capacitance for High Frequency Operation
- Ease of Temperature Sensing by Embedded NTC
- Copper Baseplate and AlN-AMB Insulator
- Positive Temperature Coefficient Device
- RoHS Compliant and Halogen Free

System Benefits

- Higher System Efficiency
- Increase Parallel Device Convenience
- Temperature Independent Switching Behavior
- Allow High Frequency Operation
- Realize Compact and Lightweight Systems

Applications

- Induction Heating
- Motor Drives
- Solar and Wind Inverters
- UPS and SMPS
- Traction

Package 62mm x 106mm x 30mm



Part Number	Package	Marking
HSM150M12BM2	Half-Bridge Module	HSM150M12BM2

Maximum Ratings ($T_c = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Notes
V_{DSmax}	Drain - Source Voltage	1.2	kV		
V_{GSmax}	Gate - Source Voltage	-10/+25	V	Absolute Maximum values	
V_{GSop}	Gate - Source Voltage	-5/+20	V	Recommended Operational Values	
I_D	Continuous Drain Current	215	A	$V_{GS} = 20 V, T_c = 25^{\circ}C$	
		150		$V_{GS} = 20 V, T_c = 90^{\circ}C$	
$I_{D(pulse)}$	Pulsed Drain Current	800	A	Pulse width t_p limited by T_{jmax}	
T_{jmax}	Junction Temperature	150	$^{\circ}C$		
T_c, T_{STG}	Case and Storage Temperature Range	-40 to +125	$^{\circ}C$		
V_{isol}	Case Isolation Voltage	3.0	kV	AC, 50 Hz, 1 min	
L_{Stray}	Stray Inductance	15	nH	Measured between terminals 2 and 3	
P_D	Power Dissipation	1660	W	$T_c = 25^{\circ}C, T_j = 150^{\circ}C$	

Electrical Characteristics ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
V_{DSS}	Drain - Source Blocking Voltage	1.2			kV	$V_{GS} = 0\text{ V}, I_D = 2\text{ mA}$	
$V_{GS(th)}$	Gate Threshold Voltage	1.8	2.5		V	$V_{DS} = 10\text{ V}, I_D = 15\text{ mA}$	
I_{DSS}	Zero Gate Voltage Drain Current		600	2000	μA	$V_{DS} = 1.2\text{ kV}, V_{GS} = 0\text{ V}$	
			1500	3000		$V_{DS} = 1.2\text{ kV}, V_{GS} = 0\text{ V}, T_J = 150^\circ\text{C}$	
I_{GSS}	Gate-Source Leakage Current		1.5	100	nA	$V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$	
$R_{DS(on)}$	On State Resistance		12	14	m Ω	$V_{GS} = 20\text{ V}, I_{DS} = 300\text{ A}$	
			17			$V_{GS} = 20\text{ V}, I_{DS} = 300\text{ A}, T_J = 150^\circ\text{C}$	
g_{fs}	Transconductance		60		S	$V_{DS} = 20\text{ V}, I_{DS} = 150\text{ A}$	
			55			$V_{DS} = 20\text{ V}, I_D = 150\text{ A}, T_J = 150^\circ\text{C}$	
C_{iss}	Input Capacitance		13.3		nF	$V_{GS}=0\text{V}, V_{DS}=800\text{V}$ $f=1\text{MHz}, V_{AC}=25\text{mV}$	
C_{oss}	Output Capacitance		0.7				
C_{rss}	Reverse Transfer Capacitance		0.12				
E_{on}	Turn-On Switching Energy		380		UJ	$V_{DS}=800\text{V}, V_{GS}=0/20\text{V},$ $I_D=120\text{A},$ $RG(ext)=0.5\ \Omega$	
E_{off}	Turn-Off Switching Energy		420		UJ		
$R_{G(int)}$	Internal Gate Resistance		0.2		Ω	$ff=1\text{MHz}, V_{AC}=25\text{mV}$	
Q_{GS}	Gate-Source Charge		180		nC	$V_{DS}=800\text{V},$ $V_{GS}=-5/+20\text{V},$ $I_D=120\text{A}$	
Q_{GD}	Gate-Drain Charge		390				
Q_G	Total Gate Charge		750				
$t_{d(on)}$	Turn-on delay time		250		ns	$V_{DS}=800\text{V}, V_{GS}=-4/20\text{V},$ $I_D=120\text{A}, RL=6.7\ \Omega,$ $RG(ext)=0.5\ \Omega$	
t_r	Rise Time		100		ns		
$t_{d(off)}$	Turn-off delay time		250		ns		
t_f	Fall Time		43		ns		
V_{SD}	Diode Forward Voltage		1.6	2.0	V	$V_{GS}=-5\text{V}, I_S=120\text{A}, T_J=25^\circ\text{C}$	
			2.3			$V_{GS}=-5\text{V}, I_S=120\text{A}, T_J=25^\circ\text{C}$	
Q_C	Total Capacitive Charge		1.0		μC		

Note: The reverse recovery is purely capacitive

Thermal Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
R_{thJCM}	Thermal Resistance Junction-to-Case for MOSFET		0.070	0.075	$^\circ\text{C/W}$	$T_c = 90^\circ\text{C}, P_D = 150\text{ W}$	
R_{thJCD}	Thermal Resistance Junction-to-Case for Diode		0.073	0.076		$T_c = 90^\circ\text{C}, P_D = 130\text{ W}$	

Schematic Package Dimensions (mm)

